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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/919,372	07/31/2001	Vadim Gutnik	5347-205	2525
20792 7:	590 06/07/2006		EXAMINER	
MYERS BIGEL SIBLEY & SAJOVEC			CHUNG, PHUNG M	
PO BOX 37428 RALEIGH, NO			ART UNIT PAPER NUMBE	
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			DATE MAIL ED: 06/07/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)
Office Action Summary		09/919,372	GUTNIK ET AL.
		Examiner	Art Unit
		Phung My Chung	2138
Period fo	The MAILING DATE of this communication app	ears on the cover sheet with the c	orrespondence address
A SH WHIC - Exte after - If NO - Failu Any	IORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DA resisting image in a suitable under the provisions of 37 CFR 1.13 resix (6) MONTHS from the mailing date of this communication. Depriod for reply is specified above, the maximum statutory period we use to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing led patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim vill apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONED	I. lely filed the mailing date of this communication. O (35 U.S.C. § 133).
Status			
2a)□	Responsive to communication(s) filed on <u>03 M</u> . This action is FINAL . 2b) This Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final. nce except for formal matters, pro	
Disposit	ion of Claims		
5)□ 6)⊠ 7)⊠ 8)□ Applicati 9)□	Claim(s) 1-26 is/are pending in the application. 4a) Of the above claim(s) is/are withdray Claim(s) is/are allowed. Claim(s) 1,7,8,14,19,25 and 26 is/are rejected. Claim(s) 2-6, 9-13, 15-18 and 20-24 is/are objection and/or are subject to restriction and/or ion Papers The specification is objected to by the Examiner The drawing(s) filed on is/are: a) acceeds Applicant may not request that any objection to the original papers.	wn from consideration. ected to. r election requirement. r. epted or b)□ objected to by the E	
44)	Replacement drawing sheet(s) including the correcti		
	The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.
12) a)[Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority documents application from the International Bureau See the attached detailed Office action for a list of	s have been received. s have been received in Application ity documents have been received (PCT Rule 17.2(a)).	on No d in this National Stage
2) D Notice 3) Inform	t(s) e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	4) Interview Summary (Paper No(s)/Mail Dat 5) Notice of Informal Pa 6) Other:	te´.

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Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1, 7-8, 14, 19 and 25-26 are rejected under 35 U.S.C. 102(b) as being anticipated by Llewellyn (5,329,251).

As per claims 1 and 7, Llewellyn discloses a clock distribution circuit, comprising:

A first clock circuit that is configured to generate a first clock signal responsive to an error signal;

A second clock circuit that is configured to generate a second clock signal responsive to the error signal; and

A phase detector circuit that connects the first clock circuit and the second clock circuit and is configured to generate the error signal responsive to the first and the second clock signals. (See col. 3, lines 29-33, lines 63-68).

As per claim 8, Llewellyn disclose a clock distribution circuit comprising:

A plurality of phase detector circuit;

A plurality of clock circuits..., the respective ones of the plurality of clock circuits being configured to generate respective ones of the plurality of clock signals responsive to respective ones of the plurality of error signals that are generated by the respective ones of the plurality of the phase detector circuits that directly connect the respective

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ones of the plurality of the clock circuits to the at least one other of the plurality of clock circuits. (See Fig. 3, col. 6, line 42 to col. 8, line 57).

As per claims 14, 19 and 25-26, these claims are rejected under similar rationale as set forth in claim 1.

- 3. Claims 2-6, 9-13, 15-18 and 20-24 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 4. Applicant's arguments with respect to claims 1-26 have been considered but are most in view of the new ground(s) of rejection.
- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phung My Chung whose telephone number is 571-272-3818. The examiner can normally be reached on Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on 571-272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Phung My Chung

Primary Patent Examiner

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